INTRODUCTION
Prior Art processors and microprocessors have not tried to balance hardware performance and OS performance at the same time. By taking into consideration the costs and benefits of implementing functions in hardware and in the operating system at the same time, this leads to optimizations resulting in a higher performance operating system, and low power requirements. The cost is a modest increase in hardware complexity.

CONCEPT
The present invention is a microprocessor architecture for efficiently running an operating system. The improved architecture provides higher performance, improved operating system efficiency, enhanced security, and reduced power consumption.

POTENTIAL MARKET
• Microprocessors and computer hardware industries

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INVENTION OVERVIEW
The present microprocessor architecture invention parallelizes the operations typically used in software by an operating system to significantly improve the performance of an operating system context switch. A second benefit of the new architecture is hardware based information assurance.
• Provides higher performance.
• Improved operating system efficiency
• Enhanced security
• Reduced power consumption.
• U.S. Patent Number: 9,122,610 B2
• Application Number: 14/029,053
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FOR FURTHER INFORMATION:
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